7-52-33-09

UA89C000 Series EGA Display Card's Chip Set

FEATURES

- 2 Micron CMOS Technology
- Bit-mapped Graphics in Four Planes
- Supports IBM Color Graphics; Monochrome Adapter and Enhanced Graphics Adapter Mode
- 720x350 Resolution for IBM Monochrome Display Adapter
- 640x200 Resolution 16 Color for IBM Color Graphics Adapter
- 640x350 Resolution 64 Color for IBM Enhanced Graphics Adapter
- Supports 256K Bytes of Memory by Using 4416/4464 DRAM
- Light Pen Interface
- Total 5 Chips Set Solution
- All 40 Pin Dip Package
- Single +5V Power Supply
- TTL Compatible

			Ι.
Vcc 🗖	1	40	i⊐ co
DOTCLK	2	39	1 C1
ATR7 🗖	3	38	1 C2
ATR6 🗖	4	37	1 C3
ATR5 🗖	5	36	1 C4
ATR4 🗖	6	35	1 C5
ATR3 🗖	7	34	D C6
ATR2 🗖	8	33	D C7
ATR1 🗖	9 L A	32	
ATRO 🗖	10 88 11 Q	31	DE
	11 <u>Ř</u>	30	CURSOR
iow 🗖	12 9	29	BLANK
LG 🗖	13	28	D D2
CRTLATCH	14	27	D D1
ČATRS/L 🗖	15	26	1 D5
RRED 🗖	16	25	D D4
GGREEN	17	24] D3
BBLUE 🗖	18	23	D D0
RED.	19	22	
GND 🗖	20	21	GREEN
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PIN CONFIGURATION

			-
DE 🗖	1	40	
CRTINT	2	39	
MADD0 🗖	3	38	SINC
MADD1 🗖	4	37	🗗 BLANK
MADD2 🗖	5	36	
MADD3 🗖	6	35	
MADD4	7	34	
MADD5	8	33	D 0
MADD6	۹ 🗧	32	D D1
MADD7	10 11 12 000	31	
GND 🗖	11 Ö	30	D D2
MADD8	12 0	29	D D3
MADD9		28	D D4
MADD 10 🗖	14	27	D5
MADD11 🗖	15	26	🗖 D6
/ADD12 🗖	16	25	D D7
/ADD13 🗖	17	24	i iowi 🗖
/ADD14 🗖	18	23	LPST
/ADD15 🗖	19	22	ССГК
REF 🗖	20	21	RESET
	in the second	-	

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MEMR	1.5.2	40		iow 🗖	1 4	ol⊐ v _{cc}
	2	39	CRA15	D0 🗖	2 3	MODO (M2DO)
CG 🗖	3	38	CRA14	D1 🗖	3 31	3 MOD1 (M2D1)
A0 🗖	4	37	CPA15	. D2 🗖	4 3	7 MOD2 (M2D2)
ATRS/L	5	36	CPA14	D3 🗖	5 3	5 D MOD3 (M2D3)
CCLK 🗖	6	35	ACASO (NC)*	D4 🗖	6 31	5 MOD4 (M2D4)
SYNC 🗖	7	34	ACASI (ADD15)*	D5 🗖	7 34	MOD5 (M2D5)
s/ī 🗖	8	33	ACAS2 (ADD14)*	D6 🗖	8 _ 3	MOD6 (M2D6)
CRT/CPU	9 5	32	ACAS3 (ASAS)*	D7 🗂		2 MOD7 (M2D7)
GND 🗖	10 88	31] GND	GND 🗖	10 👸 3	
(NC)* BCAS3 🗖		30	RAS3	CHAIN 🗖	10 89 3 11 00 30 12 00 29	M1D0 (M3D0)
(BDD15)* BCAS2 🗖	12 8	29	RAS2	GRAPHICS	12 8 29	M1D1 (M3D1)
(BDD14)* BCAS1 🗖	13	28	RAS1	A0 🗖	13 28	M1D2 (M3D2)
(BCAS)* BCASO 🗖	14	27	RASO	MDSEL	14 2.	M1D3 (M3D3)
D3 🗖	15	26	D WE		15 26	5 🖬 M1D4 (M3D4)
D2 🗖	16	25		WE 🗖	16 25	M1D5 (M3D5)
D1 🗖	17	24	CPU LATCH	CPU/LATCH	17	M1D6 (M3D6)
D0 🗖	18	23	CPT LATCH	CRT LATCH	18 23	M1D7 (M3D7)
iow 🗖	19	22	RDY	s/E 🗖	19 22	CO (C2)
CLKIN 🗖	20	21	🗇 DOT CLK	DOTCLK	20 21	C1 (C3)